

Memory Module Specifications

KSM29RD4/32MEI

32GB 2Rx4 4G x 72-Bit PC4-2933

CL21 Registered w/Parity 288-Pin DIMM

DESCRIPTION

Kingston's KSM29RD4/32MEI is a 4G x 72-bit (32GB) DDR4-2933 CL21 SDRAM (Synchronous DRAM) registered w/ parity, 2Rx4, ECC, memory module, based on thirty-six 2G x 4-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR4-2933 timing of 21-21-21 at 1.2V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

FEATURES

- Power Supply: VDD = 1.2V
- VDDQ = 1.2V
- VPP = 2.5V
- VDDSPD = 2.25V to 2.75V
- Functionality and operations comply with the DDR4 SDRAM datasheet
- 16 internal banks
- Bank Grouping is applied, and CAS to CAS latency (tCCD_L, tCCD_S) for the banks in the same or different bank group accesses are available
- Data transfer rates: PC4-2933, PC4-2666, PC4-2400, PC4-2133, PC4-1866, PC4-1600
- Bi-Directional Differential Data Strobe
- 8 bit pre-fetch
- Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
- Supports ECC error correction and detection
- On-Die Termination (ODT)
- Temperature sensor with integrated SPD
- This product is in compliance with the RoHS directive.
- Per DRAM Addressability is supported
- Internal Vref DQ level generation is available
- Write CRC is supported at all speed grades
- CA parity (Command/Address Parity) mode is supported

SPECIFICATIONS

CL(IDD)	21 cycles
Row Cycle Time (tRCmin)	45.75ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	350ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Maximum Operating Power	*
UL Rating	94 V - 0
Operating Temperature	0° C to +85° C
Storage Temperature	-55° C to +100° C

* See IDD Table (page2)

Module Assembly

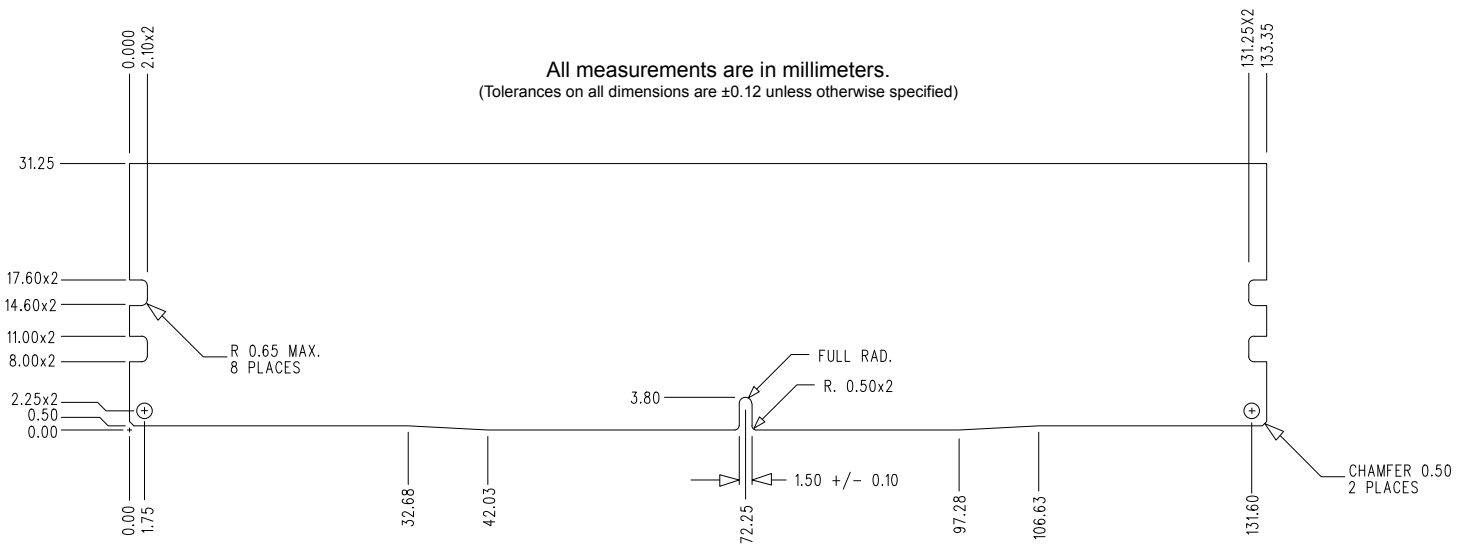
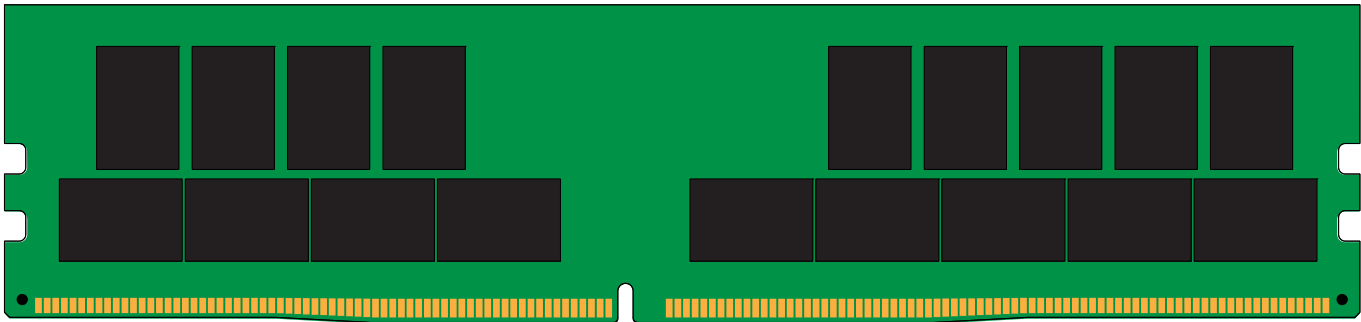
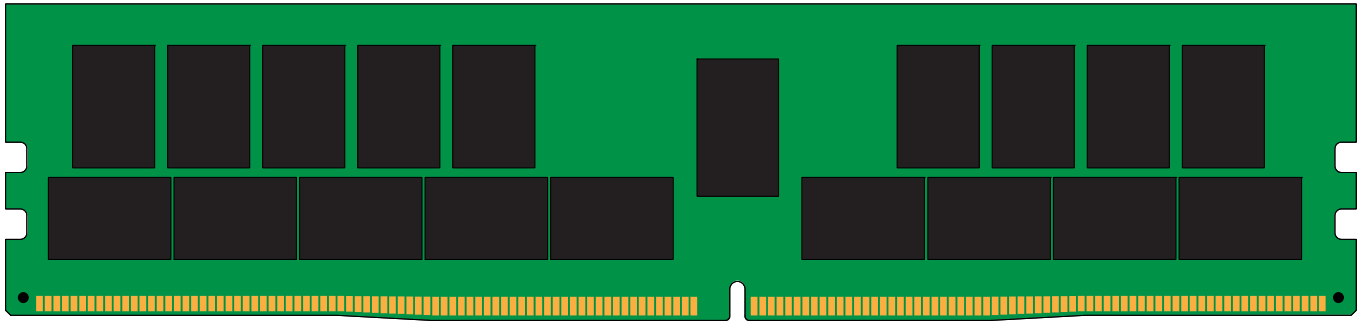
DRAM: MICRON (E-DIE)
RCD: IDT

IDD Specifications

Symbol	2933	Units
I_{DD0}^1	1170	mA
I_{PP0}^1	108	mA
I_{DD1}^1	1404	mA
I_{DD2N}^2	1152	mA
I_{DD2NT}^1	1152	mA
I_{DD2P}^2	792	mA
I_{DD2Q}^2	936	mA
I_{DD3N}^2	1440	mA
I_{PP3N}^2	108	mA
I_{DD3P}^2	1116	mA
I_{DD4R}^1	2952	mA
I_{DD4W}^1	2610	mA
I_{DD5R}^1	1278	mA
I_{PP5R}^1	144	mA
I_{DD6N}^2	1224	mA
I_{DD6E}^2	2088	mA
I_{DD6R}^2	756	mA
I_{DD6A}^2	309.6	mA
I_{DD6A}^2	756	mA
I_{DD6A}^2	1116	mA
I_{DD6A}^2	2088	mA
I_{PP6X}^2	180	mA
I_{DD7}^1	4266	mA
I_{PP7}^1	306	mA
I_{DD8}^2	648	mA

Notes: 1. One module rank in the active IDD/PP, the other rank in IDD2P/PP3N.
2. All ranks in this IDD/PP condition.

MODULE DIMENSIONS



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